Application No. <u>09/910,732</u> Attorney's Docket No. <u>012050-077</u>

REMARKS

Claims 1-67 remain pending in the application. The specification has been amended to add a brief description of Fig. 13c. Support for this amendment is found, for example, in Fig. 13c itself (which clearly shows a flowchart for step "S3") as well as in the detailed description text spanning page 36, line 24 through page 37, line 16.

The application is believed to be in condition for allowance. Favorable examination on the merits is respectfully requested.

Respectfully submitted,

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Attachment to Preliminary Amendment dated October 12, 2001

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Paragraph Beginning at Page 17, line 21

Fig. la shows an OFDM transmission system SYS in accordance with the prior art[;].

Fig. lb shows the preamble part PER of a burst BST shown in Fig. la[;].

Fig. 2a shows the key parameters of the physical layer modes of an exemplification HIPERLAN/2 OFDM system[;].

Fig. 2b shows the key parameters of the HIPERLAN/2 physical layer[;].

Fig. 3 shows a model of an equivalent communication channel on subcarrier m in OFDM symbol k[;].

Fig. 4a shows a block diagram in accordance with a first aspect of the principle of the invention for determining a link quality measure on the basis of signal power variations[;].

Fig. 4b shows one example of the parameters which can be used for a signal power variation determination and a link quality measure determination[;].

Fig. 5a shows a block diagram similar to Fig. 4a for the determination of a link quality measure depending on a SNR variation[;].

Fig. 5b shows a block diagram similar like Fig. 4a for the determination of parameters used for the signal-to-noise variation determination in Fig. 5a[;].



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The stream of the Fig. 6 shows a block diagram of another aspect of the invention where at least a second quality measure determination unit determining a link quality measure based on the signal-to-noise ratio is provided in combination with the first link quality measure determination unit[;].

Fig. 7 shows a block diagram of a processing device PRD, in particular a block diagram of the noise power determination unit PC-DET shown in Fig. 6[;].

Fig. 8 shows a block diagram of a demodulation circuitry DEMOD-CRT of an OFDM receiver as well as a remodulation unit REMOD and [an] a reencoding/ remodulation unit REINC-REMOD used for providing estimates of the subcarrier symbol information based on data-bearing subcarrier symbols within the protocol data units[;].

Fig. 9 shows a block diagram of a noise sample determination unit ZM-DET used for calculating the noise power[;].

Fig. 10 shows a mapping diagram for combining the first and second link quality measures Q1, Q2 into a common decision map[;].

Fig. 11 shows the usage of a hysteresis for link adaptation[;].

Fig. 12 shows a principle flow diagram of the steps carried out for determining the first and second link quality measures[;].

Fig. 13a shows a flowchart for calculating a signal power variance in accordance with the first embodiment of the invention[;].



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Attachment to Preliminary Amendment dated October 12, 2001

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Fig. 13b shows a flowchart for calculating the signal-to-noise variance in accordance with the second embodiment of the invention[;].

Fig. 13c shows a flowchart for determining the noise power (step S3 in Fig. 12).